

Fig. 1B

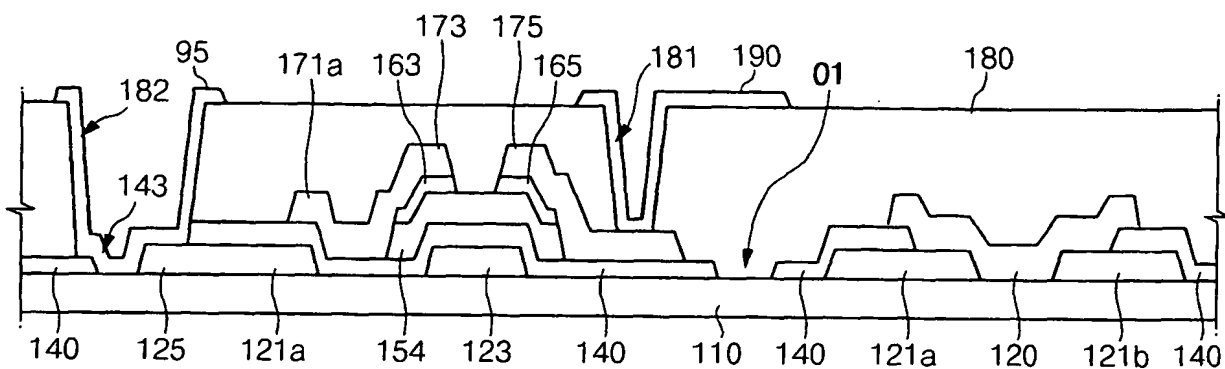
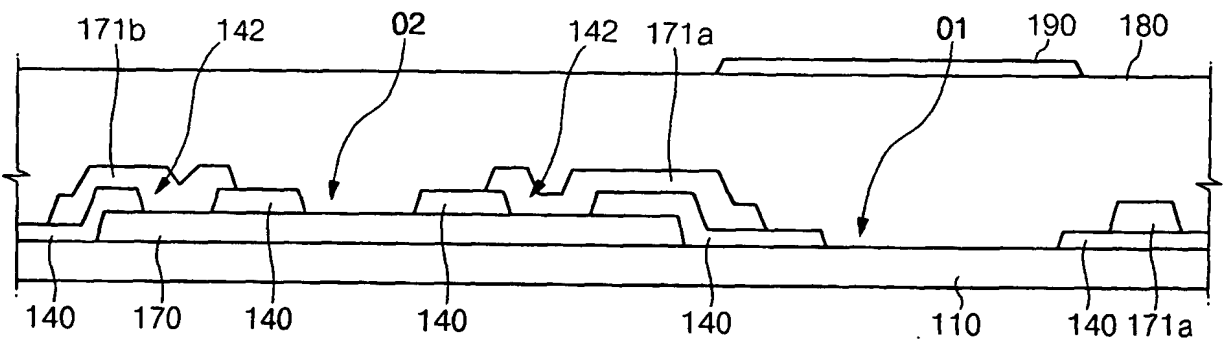


Fig. 1C



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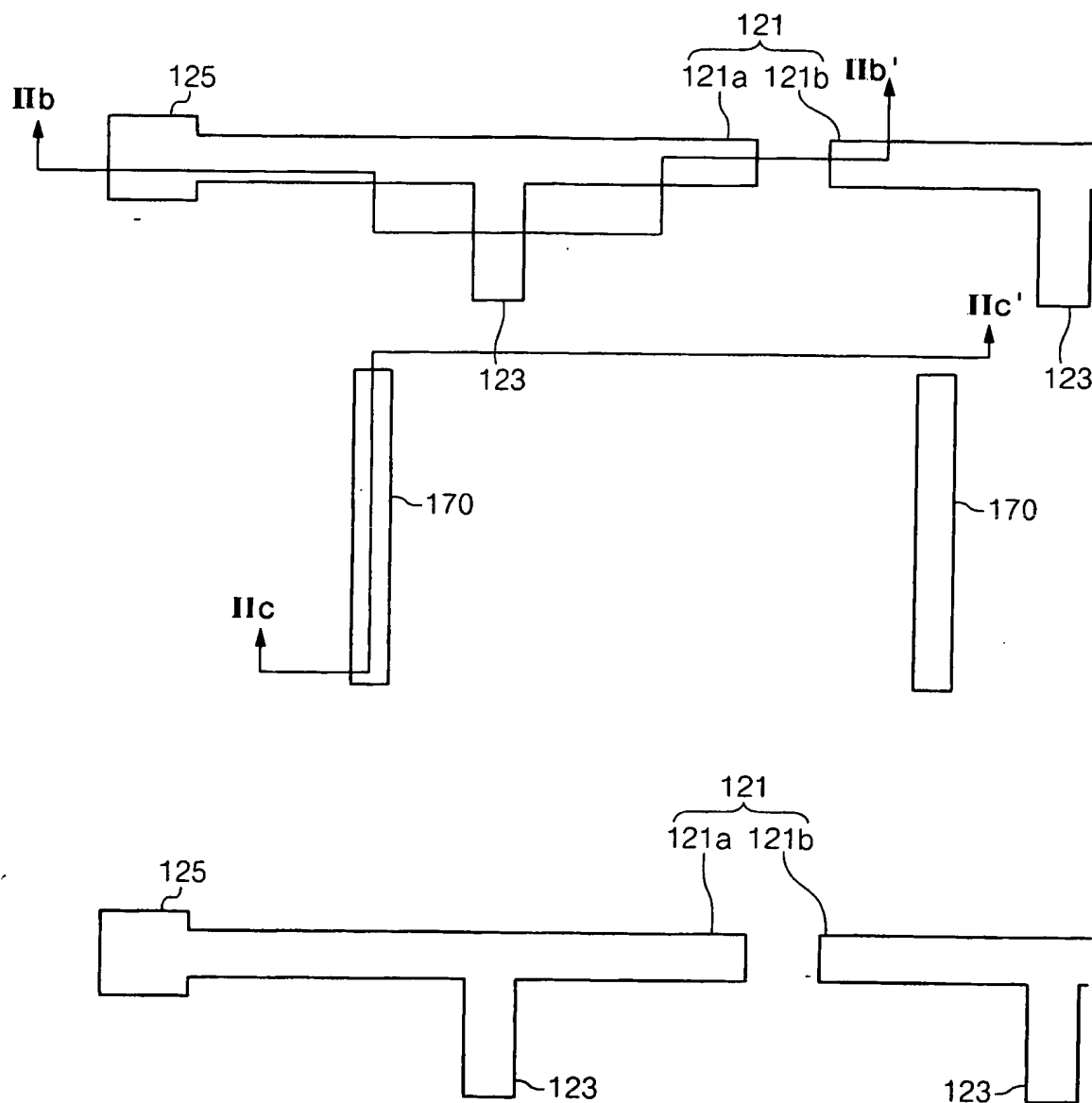
Fig. 2A

Fig. 2B

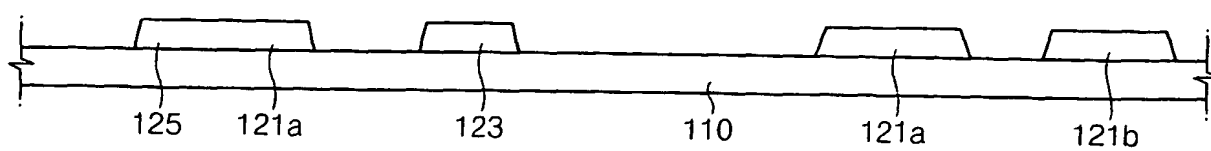


Fig. 2C

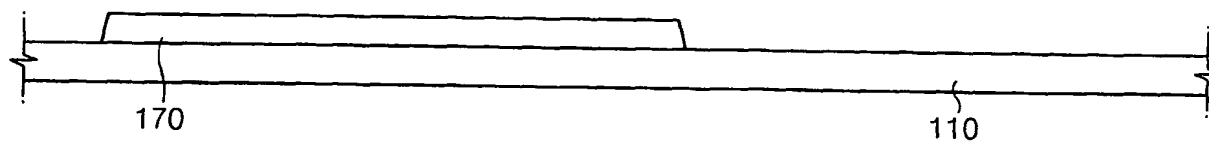
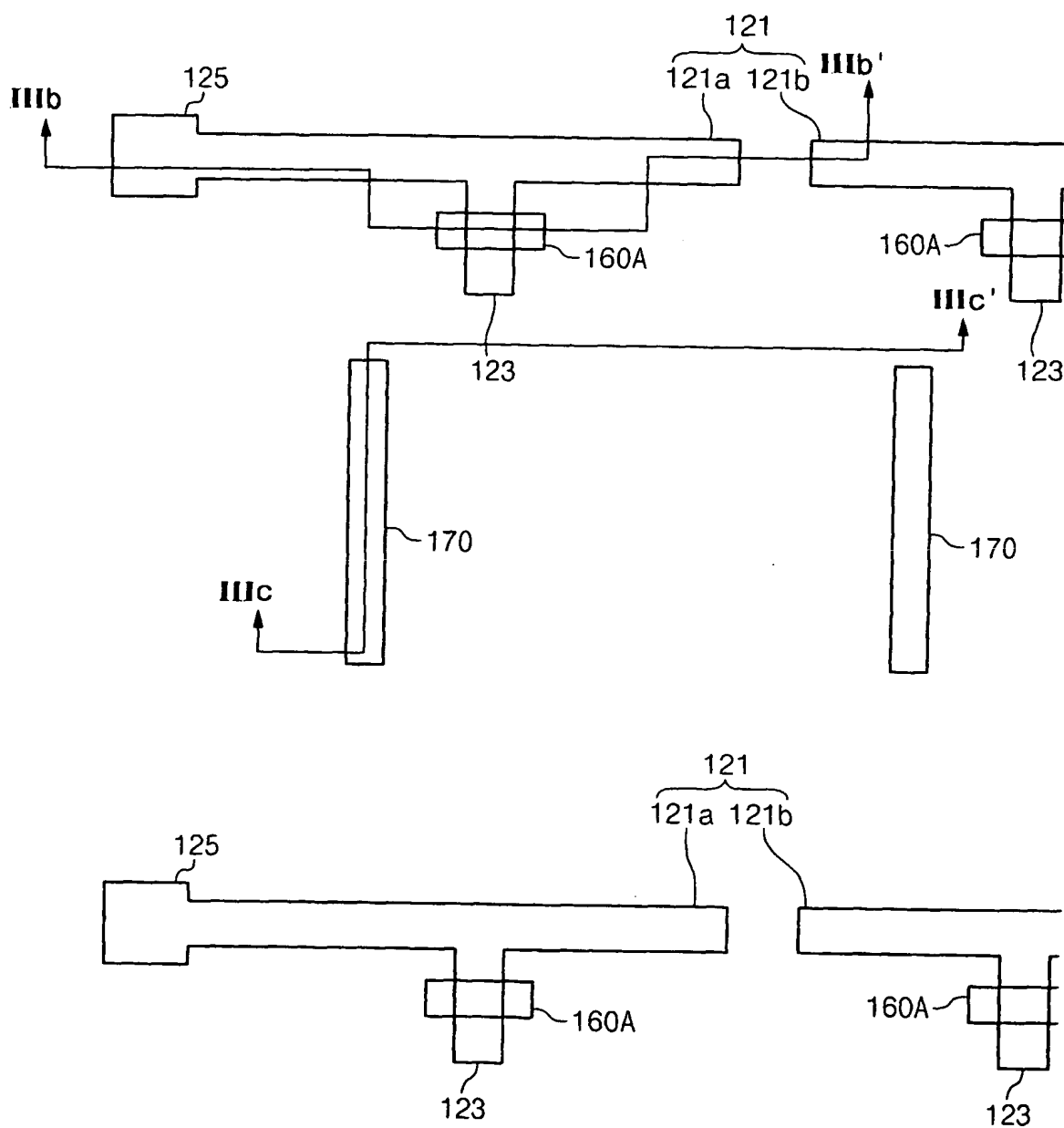


Fig. 3A



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Fig. 3B

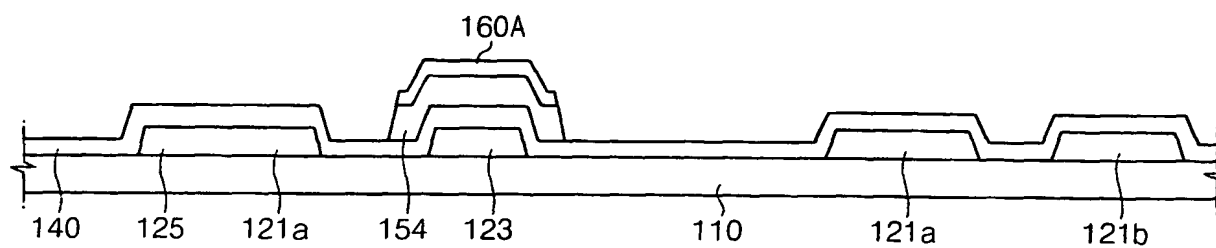
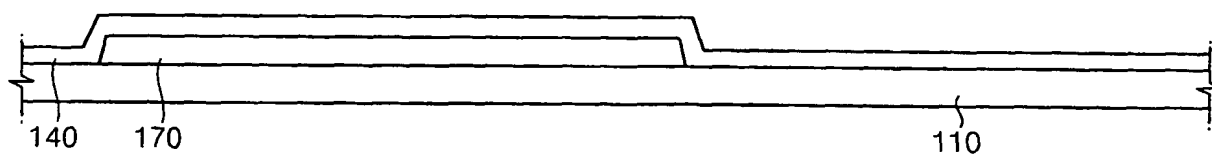
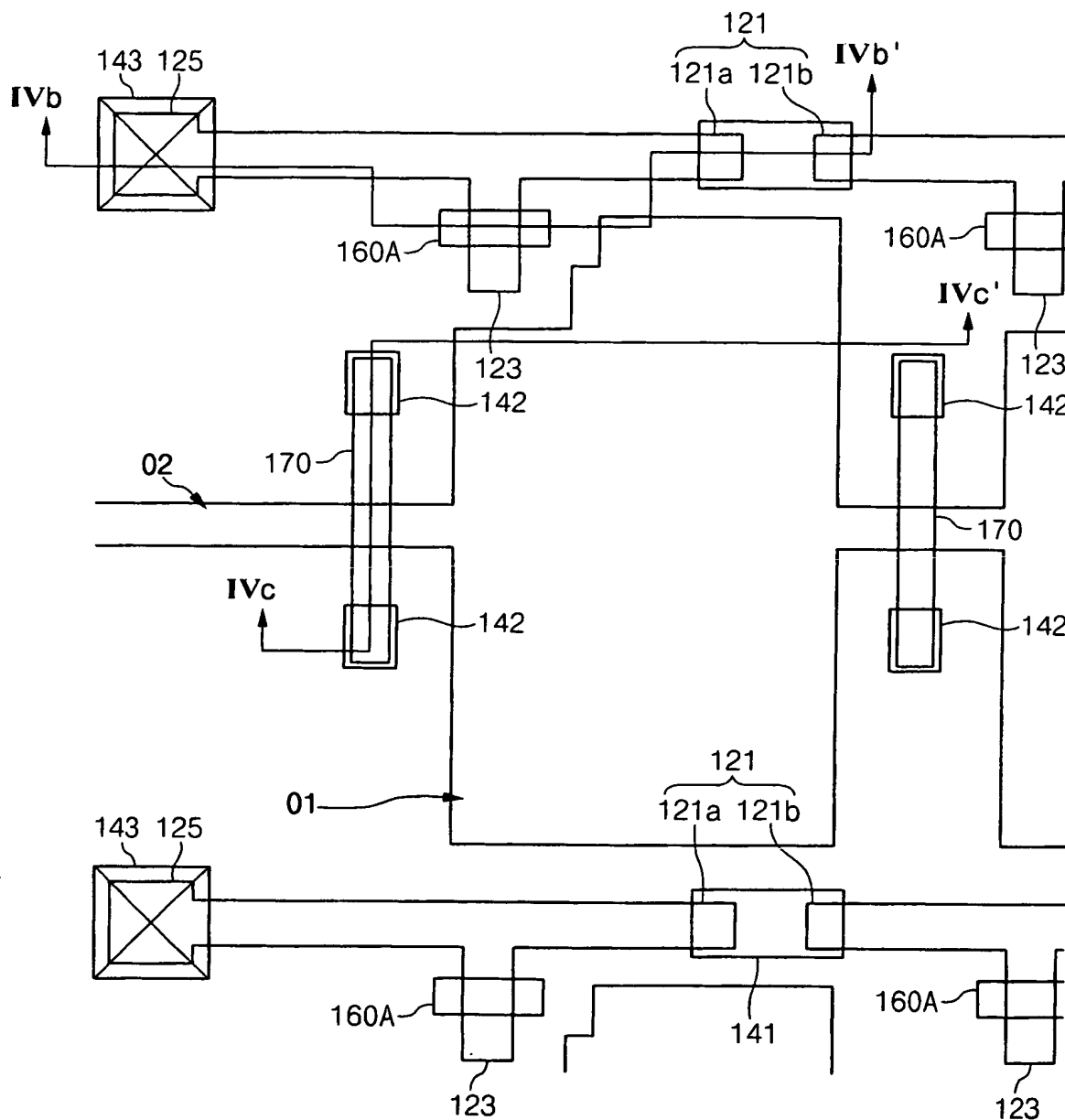


Fig. 3C



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Fig. 4A



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Fig. 4B

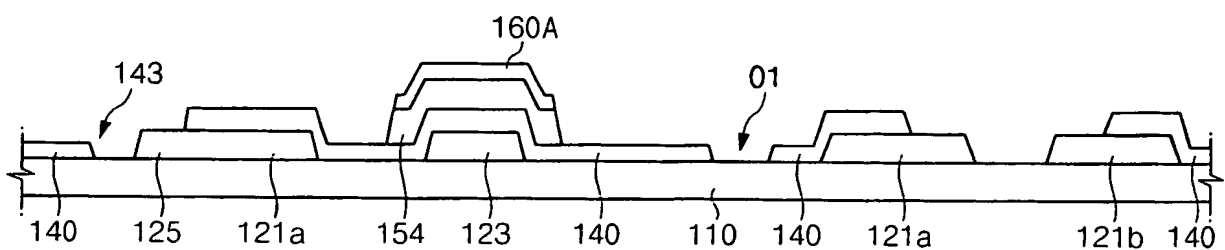


Fig. 4C

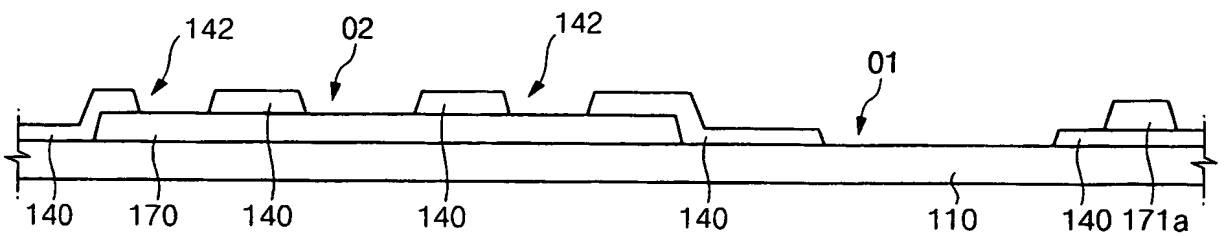


Figure 1 is a schematic diagram of a semiconductor device. It shows two vertical columns of components. Each column contains a series of rectangular blocks (120, 141, 121, 123, 142, 170, 171, 173, 175) connected by horizontal lines. The left column is connected to a voltage source V_b and a voltage source V_c . The right column is connected to a voltage source $V_{b'}$ and a voltage source $V_{c'}$. The diagram also shows a central vertical line (179) and a horizontal line (02).

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Fig. 5B

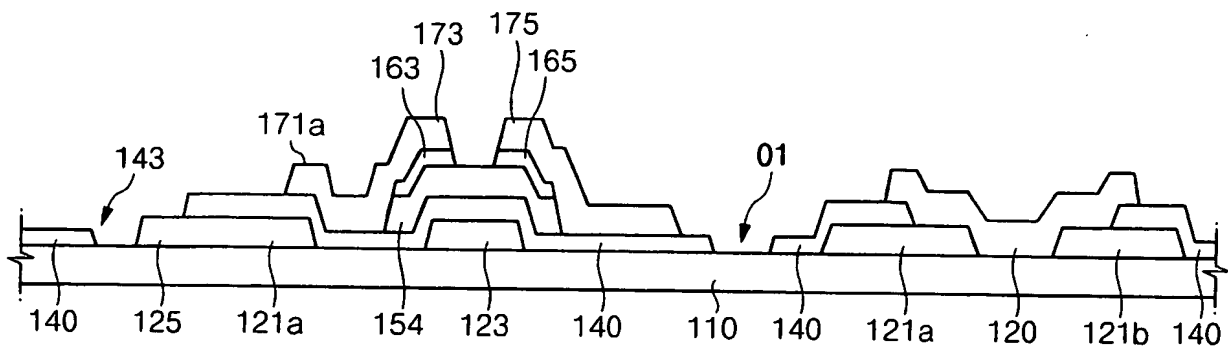
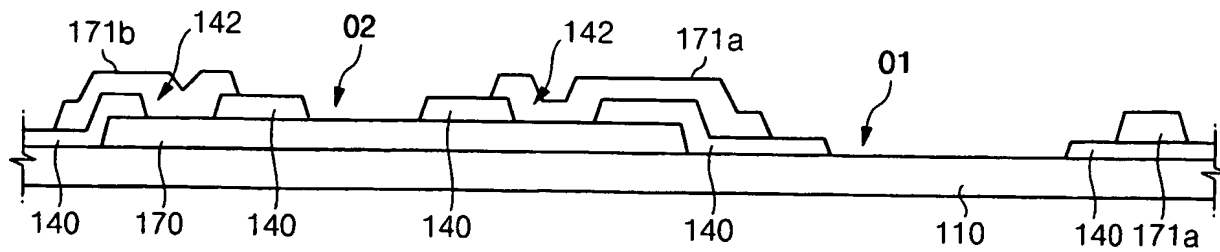


Fig. 5C



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Fig. 6

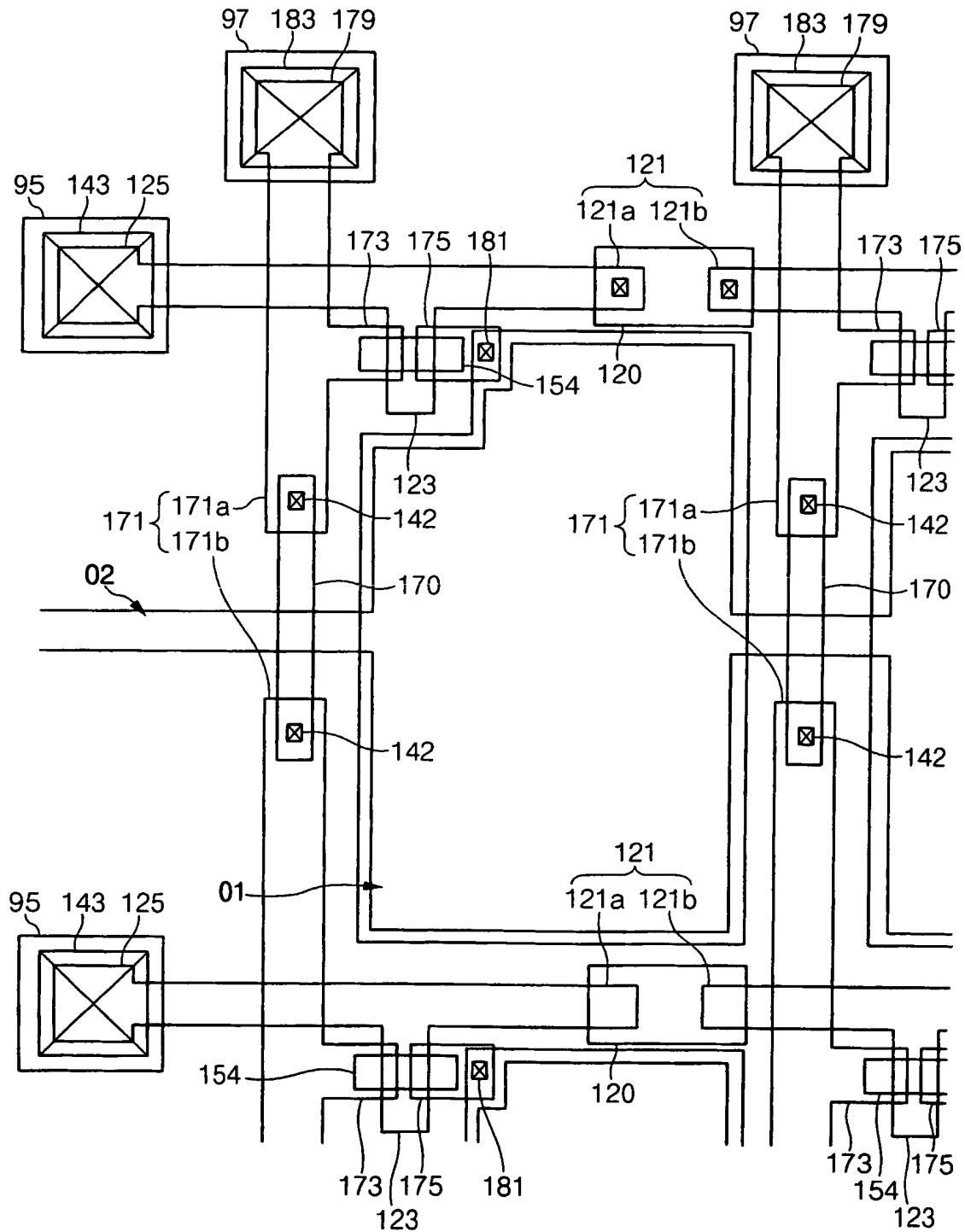
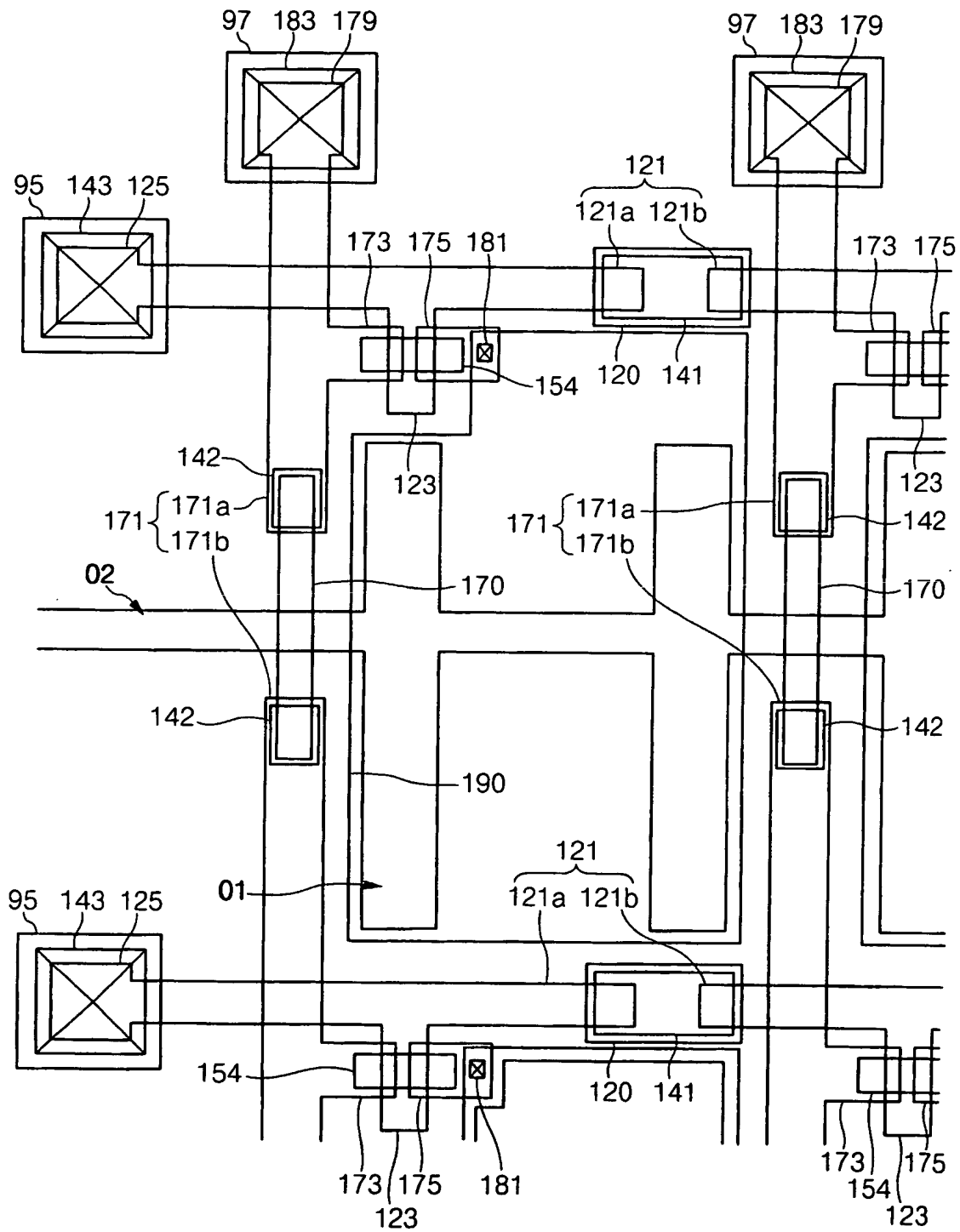
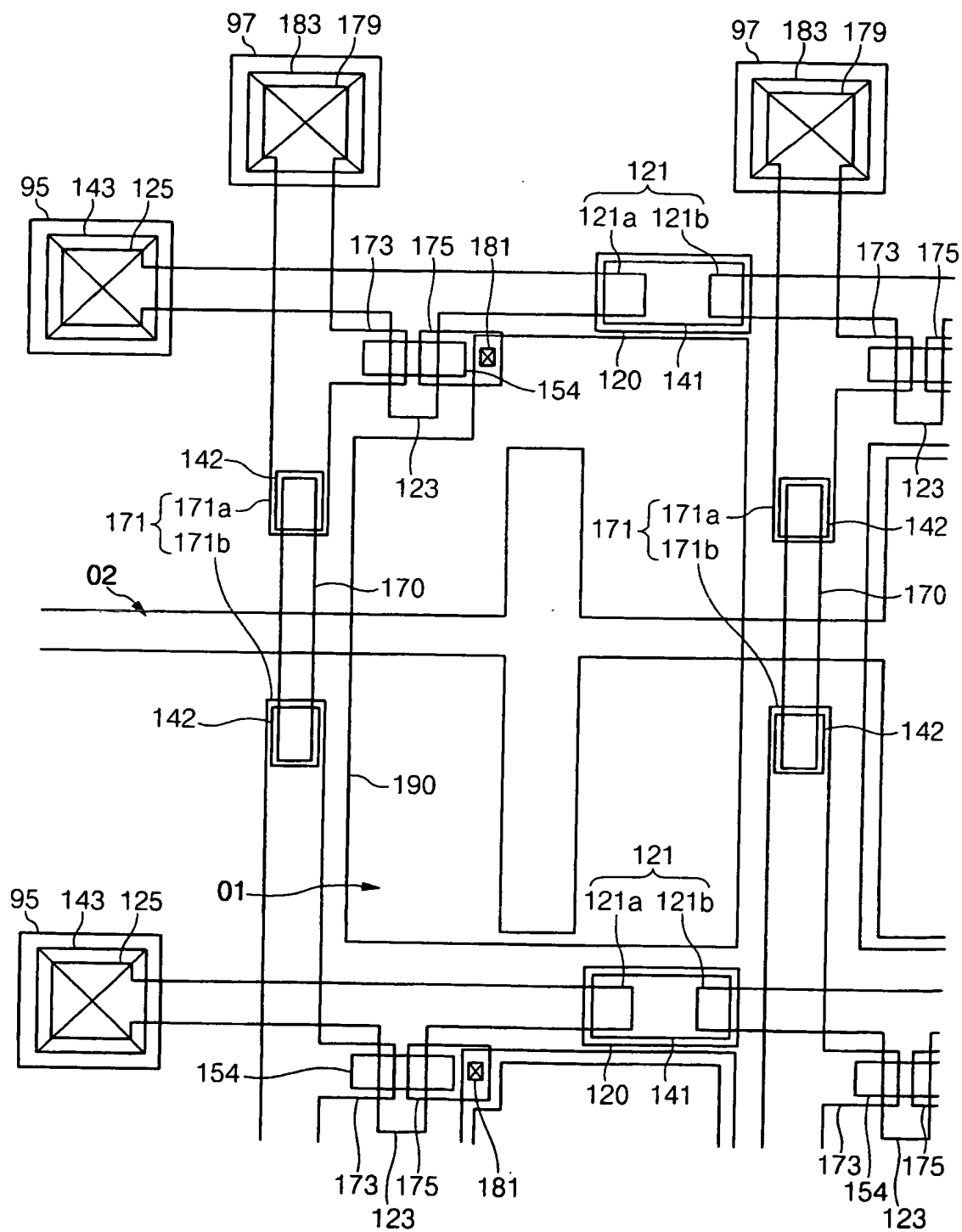


Fig. 7



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Fig. 8

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Fig. 9